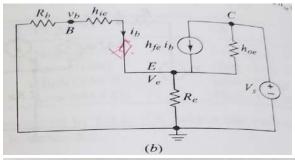
## Thapar University, Patiala

## DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Course Code: UEC 301; Course Name: Analog Electronics B. Tech, ECE (III-Sem), "Tutorial Sheet No. - 5"

Q1. For the circuit of fig. (1), (a) find the expression for  $V_b$  and  $V_e$  in terms of  $V_s$  and other circuit parameters. (b) If  $R_b = 50 \ k\Omega$ , and  $R_e = 1 \ k\Omega$ , determine  $\frac{V_b}{V_s}$  and  $\frac{V_e}{V_s}$ . Use h-parameter model of CE equivalent circuit. Given  $h_{ie} = 1k\Omega$ ,  $h_{re} = 0$ ,  $h_{fe} = 100$ , and  $h_{oe} = \frac{1}{50} k\Omega$ .



Solution (a) Using the equivalent circuit of Fig. 8.6a, and noting that  $h_{re} = 0$ , the given circuit can be re-drawn shown in Fig. 8.24b. Writing KCL at the emitter node yields,

$$\frac{V_e}{R_e} + \frac{V_e}{R_b + h_{ie}} + \frac{h_{fe}V_e}{R_b + h_{ie}} + \left(V_e - V_s\right)h_{oe} = 0$$

where we have used  $i_b = -\frac{V_e}{R_b + h_{ie}}$ . Solving for  $V_e$ , we get

$$V_e = \frac{h_{oe}}{\left(\frac{1}{R_e} + \frac{\left(1 + h_{fe}\right)}{R_b + h_{ie}} + h_{oe}\right)} \cdot V_s$$

Noting that  $V_b = \frac{R_b}{R_b + h_{ie}} V_e$ , yields

$$V_b = \frac{R_b h_{oe}}{\left(R_b + h_{ie}\right) \left(\frac{1}{R_e} + \frac{\left(1 + h_{fe}\right)}{R_b + h_{ie}} + h_{oe}\right)} \cdot V_s$$

These numbers show that a signal applied to the collector terminal appears significantly attenuated at the base and emitter terminals. This is, of course, the reason that one never uses the collector of a transistor as an input amount of a transistor is linear, the reciprocity theorem is

Note also that because the small-signal equivalent circuit of a transistor is linear, the reciprocity theorem is applicable to it, so that  $(v_b/v_s)$  in Fig. 8.24b is simply the reciprocal of the gain of the circuit in which the input applicable to it, so that  $(v_b/v_s)$  in Fig. 8.24b is simply the reciprocal of the gain from the base to the collector is is applied at the base, and the output taken at the collector. And since the gain from the previous high, it is expected that the gain from the collector to the base will be low (being the reciprocal of the previous pain).

Q2. The transistor of Fig.2 is connected as a CE amplifier. Find the various gains and input and output impedances using h-parameters. Given that  $R_L=10K$ ,  $R_s=1$  K,  $h_{ie}=1$ . K,  $h_{re}=2.5\times 10^{-4}$ ,  $h_{fe}=50$ ,  $h_{oe}=25$   $\mu A/V$ 

$$A_{I} = -\frac{h_{fe}}{1 + h_{oe}R_{L}} = -\frac{50}{1 + 25 \times 10^{-6} \times 10^{4}} = -40.0$$

$$R_{i} = h_{ie} + h_{re}A_{I}R_{L} = 1,100 - 2.5 \times 10^{-4} \times 40.0 \times 10^{4} = 1,000 \Omega = 1 \text{ K}$$

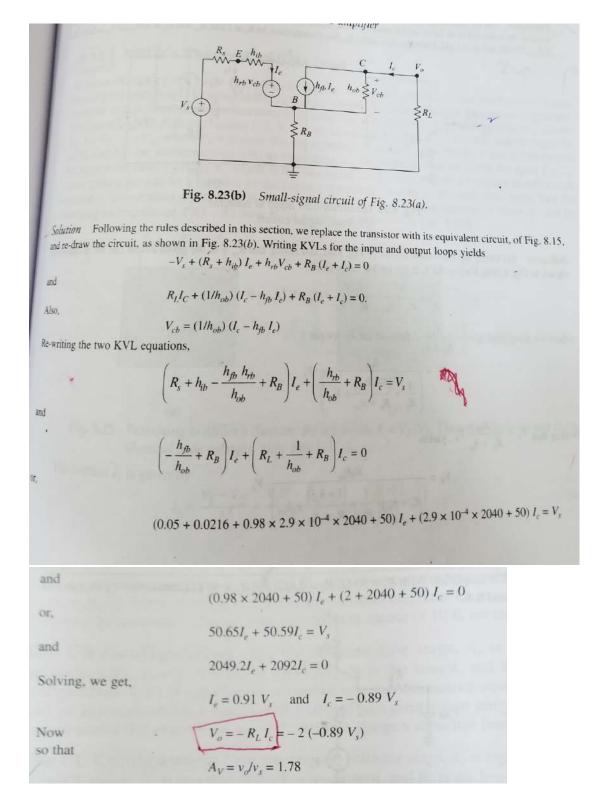
$$A_{V} = \frac{A_{I}R_{L}}{R_{i}} = -\frac{40 \times 10}{1} = -400$$

$$A_{Vs} = \frac{A_{V}R_{i}}{R_{i} + R_{s}} = -\frac{400 \times 1}{1 + 1} = -200$$

$$A_{Is} = \frac{A_{I}R_{s}}{R_{i} + R_{s}} = -\frac{40.0 \times 1}{1 + 1} = -20.0$$
Note that  $A_{Vs} = A_{fs}R_{L}/R_{s}$ .
$$Y_{o} = h_{oc} - \frac{h_{fe}h_{re}}{h_{ie} + R_{s}} = 25 \times 10^{-6} - \frac{50 \times 2.5 \times 10^{-4}}{2,100} = 19.0 \times 10^{-6} \text{ T} = 19.0 \text{ } \mu\text{A/V}$$

$$Z_{o} = \frac{1}{Y_{o}} = \frac{10^{6}}{19.0} \Omega = 52.6 \text{ K}$$

- Q3. The circuit shown in Fig.3 is a \_\_\_\_\_amplifier. If  $R_S = 50$  ohm,  $R_L = 2$  K and  $R_B = 50$  K, then
  - (i) What is the configuration of amplifier (CE or CB or CC)
  - (ii) Determine  $A_V = v_o/v_s$  of the amplifier.



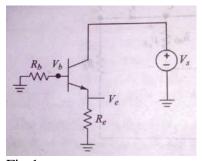


Fig.1

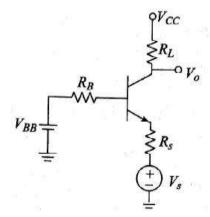


Fig.3

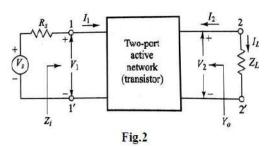


Fig.2